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Article

Flexible Compensation of Voltage and Current Unbalance and Harmonics in Microgrids

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Abstract: In recent years, the harmonics and unbalance problems endanger the voltage and current quality of power systems, due to increasing usage of nonlinear and unbalanced loads. Use of Distributed Generation (DG)-interfacing inverters is proposed for voltage or current compensation. In this paper, a flexible control method is proposed to compensate voltage and current unbalance and harmonics using the distributed generation (DG)-interfacing inverters. This method is applicable to both grid-connected and islanded Microgrids (MGs). In the proposed method, not only the proper control of active and reactive powers can be achieved, but also there is flexibility in compensating the voltage or current quality problems at DG terminals or Points of Common Coupling (PCCs). This control strategy consists of active and reactive power controllers and a voltage/current quality-improvement block. The controller is designed in a stationary ($\alpha\beta$) frame. An extensive simulation study has been performed and the results demonstrate the effectiveness of the proposed control scheme. Depending on the compensation modes, the harmonics and unbalance compensation of DG output current, MG-injected current to the grid, as well as PCC and DG voltages, can be achieved in grid-connected operation of MG while in the islanded operation, and the PCC and DG voltages compensation can be obtained through the proposed control scheme.

Keywords: Distributed Generation (DG); DG-interfacing inverter; microgrid; power quality

1. Introduction

In recent years, due to the disadvantages of conventional fossil fuels such as air pollution and extinction, the concept of benefitting from renewable energy sources (RESs) has attracted more attention [1–3]. Microgrid (MG) and Distributed Generation DG concepts have emerged for better use of the RESs and enhancing efficiency, power quality and reliability of the conventional power systems [4,5]. Since the output voltage of many of the RESs are unregulated DC or variable/high frequency AC, the integration of renewable energy to the utility grids or MGs requires power electronic converters, with an inverter as the last conversion stage in case of AC systems [6]. These inverters are usually connected to the grid by an LCL or LC filter [7,8].

On the other hand, in the presence of unbalanced, single-phase and nonlinear loads, excessive voltage unbalance and harmonics may appear in the MGs. The unbalanced and harmonic distorted voltage leads to more losses and less stability of the MG. It also causes malfunction of power electronics converters and induction motors [9]. Due to these problems, the allowed limit for voltage unbalance

recommended by the International Electrotechnical Commission (IEC) is 2%, while the recommended limit for voltage total harmonic distortion (THD) is 5% [10].

Incorporation of multifunctional interfacing inverters for compensation of power quality-related problems has been proposed recently in some research works, such as [11–16]. With proper control of these inverters, not only the extracted energy of RESs can be injected into the grid or MG, but also the improvement of voltage and current quality may be achieved. In [17], droop control for injecting the negative sequence voltage is proposed. A method based on injecting negative sequence voltage is presented in [9]. The voltage injection is based on negative sequence reactive power to share the compensation effort between DG units. The voltage quality improvement using secondary and tertiary control is also proposed in [18–21]. The compensation of unbalanced and nonlinear loads in a multi-bus islanded MG, using an adjustable resonance frequency, a droop control strategy, and a negative and harmonic sequence impedance controller, is discussed in [22].

All of the aforementioned works have been focused on only voltage or current quality improvement. In [23], a flexible voltage and current harmonic compensation approach is proposed through DG-interfacing inverters in a single-phase MG. The performance of the method is improved by using a frequency adaptive controller which is presented in [24]. In this paper, extension of control methods of [23,24] is proposed in three-phase MGs to enable unbalance and harmonic compensations. In the proposed method, four harmonic and unbalance compensation modes can be achieved, while the fundamental positive sequence components of DG output voltage can also be properly controlled. The main contributions of this paper can be highlighted as follows:

- Simultaneous control of fundamental positive and negative sequences, as well as harmonic components of voltage and current, is achieved for three-phase DG-interfacing inverters.
- A flexible voltage or current compensation of nonlinear and unbalanced loads is proposed.
- The proposed method is applicable in both grid-connected and islanded modes of three-phase MGs.
- Four unbalanced and harmonic voltage or current compensation strategies can be achieved using this method.

The extensive simulation which is implemented in MATLAB/Simulink (2016a Version, MathWorks, Natick, MA, USA) verifies the effectiveness of the power-quality improvement method in both grid-connected and islanded operation modes of an MG.

The remaining parts of this paper are organized in the following order: In Section 2, the DG-interfacing control method is introduced. Section 3 is dedicated to the design of the control system. The details of the simulation study are presented in Section 4. Finally, Section 5 concludes the paper.

2. Control Approach for DG-Interface Inverter

2.1. Power Control and Virtual Impedance

Figure 1 shows the power stage and control system of the DG-interfacing inverter. As shown in this figure, the power stage consists of a DC link, a three-phase inverter and an LCL filter. In the control system, the positive sequence active and reactive powers are calculated based on instantaneous active and reactive power theory [25].

In the control system, the droop-based voltage ($V_{droop,\alpha\beta}$) is generated by droop controllers. Virtual impedance is used for decoupling the reactive and active powers droop controllers and enhancing the dynamics of the system [26,27]. Four voltage or current unbalance and harmonic compensation modes can be selected by the “Mode Selector” switch. Since the control system is based on the $\alpha\beta$ frame, the three-phase voltages and currents should be transferred to the $\alpha\beta$ reference frame using Clark’s transformation [9].

where R_v and L_v represent the virtual resistance and inductive impedances, respectively. The reference voltage will be written by (4):

$$V_{ref,\alpha\beta} = V_{droop,\alpha\beta} - V_{v,\alpha\beta} \quad (4)$$

2.2. Proposed Flexible Controller for Unbalance and Harmonic Compensation

As shown in Figure 1, four power-quality improvement strategies can be obtained by the “Mode Selector” switch. Based on this, the following compensation modes are taken into account (the mode’s number associates with the position of “Mode Selector” switch):

- Mode 1—unbalance and harmonic compensation of Point of Common Coupling (PCC) voltage (V_{PCC}): in this mode, the unbalance and harmonic voltage compensation is achieved by using the following equation as input of flexible power quality controller (FPQC):

$$(X_{ref,\alpha\beta} - X_{\alpha\beta}) = G^- V_{PCC,\alpha\beta}^- + G_h (V_{PCC,3\alpha\beta} + \dots + V_{PCC,h\alpha\beta}) - I_{2,\alpha\beta} \quad (5)$$

where G^- and G_h represent negative sequence and harmonic virtual conductance, respectively. The extraction of the fundamental negative sequence and harmonic (FNS&H) components of V_{PCC} is performed using the Second Order Generalized Integrator (SOGI) method, which is presented in [29]. The multifunctional inverter acts as damping resistance at FNS&H components, and low-impedance paths are created for respective current components; hence, the voltage quality will be enhanced. This compensation is useful when a sensitive load is connected in PCC. It should be mentioned that in this mode, the inverter acts in current controlled mode (CCM) for FNS&H components.

- Mode 2—unbalance and harmonic compensation of DG output voltage (V_C): The capacitor voltage will be balanced if the $(0-V_{C,\alpha\beta})$ is applied as the input of FPQC. The unbalance and harmonic compensation of V_C is obtained by injecting FNS&H sequences of V_C ; hence, the performance of the inverter acts in voltage controlled mode (VCM) in FNS&H sequences. The compensation is important when a sensitive load is connected near the DG. The selection of Modes 1 or 2 is dependent on the location of the sensitive load.
- Mode 3—local unbalanced and nonlinear loads compensation: in this mode, the aim of the compensation is the unbalance and harmonic compensations of MG-injected current (I_{inj}). Using $(I_{caol} - I_2)$ as input of FPQC, the multifunctional inverter acts as an active filter in negative and harmonic sequences, therefore, the FNS&H parts of unbalanced and nonlinear loads are supplied by the DG-interfacing inverter and I_{inj} will be balanced. The mode can be activated when there is a limitation for the unbalance and harmonics current of an MG, since each system has limitations in harmonic and unbalance injections according to IEEE 519 and EN 50160 standards.
- Mode 4—the output current compensation of DG unit (I_2): in this mode, which is called the unbalance and harmonics rejection mode, the output current of DG will be compensated. This mode is important when the DG delivers maximum power to the grid, and injection of unbalance and harmonics current can increase the current of one/two phase(s) higher than the rated one.

Figure 2 shows more details of the proposed control diagram. As can be observed in this figure, the control scheme consists of three parts. The first part is related to the PS voltage control, where V_{ref} is tracked as a VCM inverter. The second part is related to unbalance and harmonic compensation, and acts as a CCM inverter except in Mode 2. As discussed before, since the compensation of V_C is achieved by injecting negative and harmonic sequence voltage, the inverter acts as VCM inverter in FNS&H sequences in Mode 2. The third part is applied to provide some damping [31]. The following equation expresses these three parallel controllers:

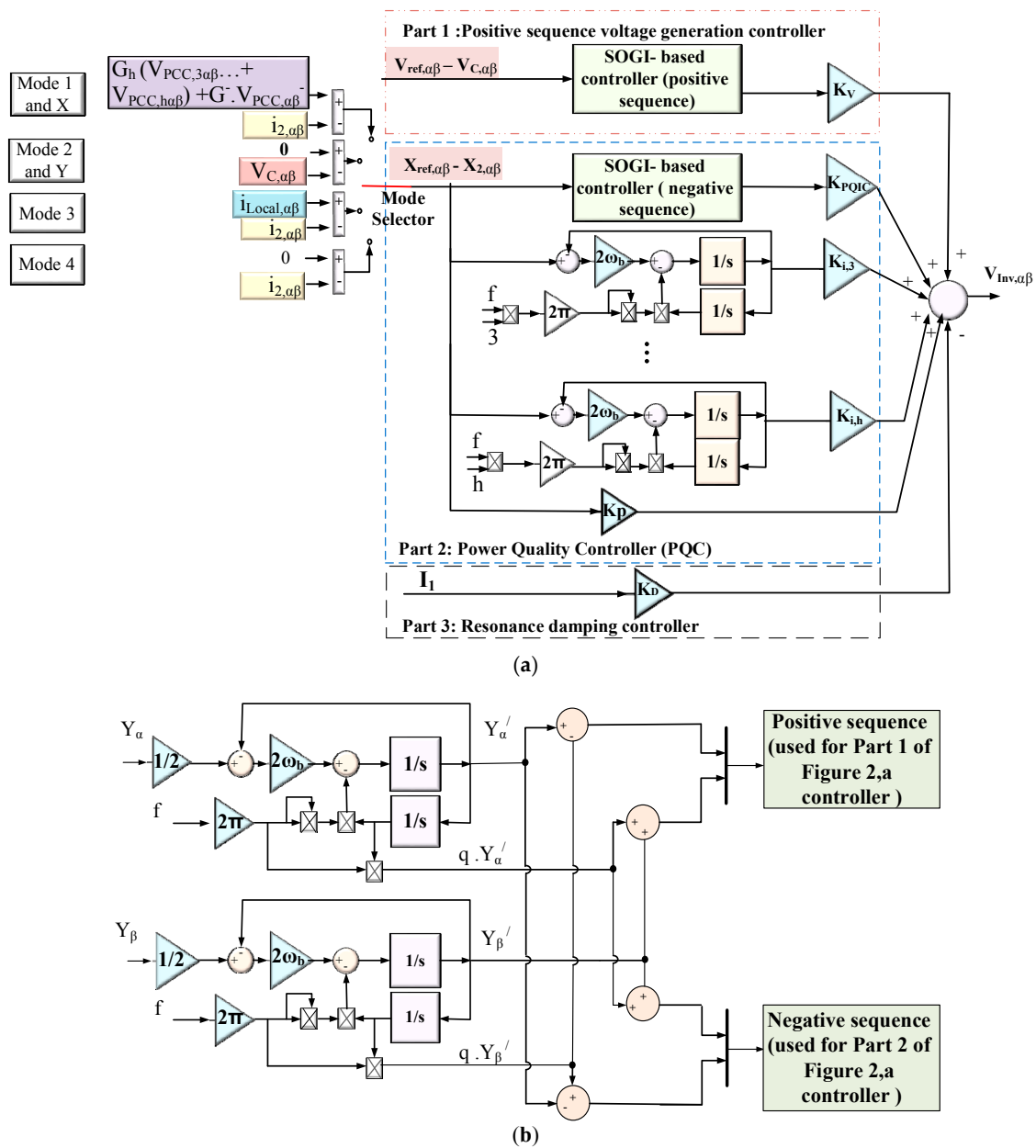


Figure 2. Proposed flexible power-quality controller, (a) general scheme of FPQC; and (b) SOGI-based controller.

$$V_{inv,\alpha\beta} = G_V(s)(V_{ref,\alpha\beta} - V_{c,\alpha\beta})^+ + G_{PQC}(s)(X_{ref,\alpha\beta} - X_{\alpha\beta}) + K_D(s)I_{1,\alpha\beta} \quad (6)$$

where G_V and G_{PQC} represent the voltage and power quality compensation controllers, respectively. K_D is a proportional gain which is used for resonance damping. As illustrated in Figure 2a, in the PS voltage and unbalance compensation parts of Power Quality Controller (PQC), SOGI-based negative and positive extraction blocks are utilized. Figure 2b shows the details of the controller. As shown in this figure, the quadrature-phase waveforms (90° lag) of the original in-phase waveforms are created, and according to the following equations, the negative and positive sequences are extracted [29]:

$$\begin{bmatrix} x_\alpha^+ \\ x_\beta^+ \end{bmatrix} = \frac{1}{2} \begin{bmatrix} 1 & -q \\ q & 1 \end{bmatrix} \begin{bmatrix} x_\alpha \\ x_\beta \end{bmatrix}, \quad (7)$$

$$\begin{bmatrix} x_{\alpha}^- \\ x_{\beta}^- \end{bmatrix} = \frac{1}{2} \begin{bmatrix} 1 & q \\ -q & 1 \end{bmatrix} \begin{bmatrix} x_{\alpha} \\ x_{\beta} \end{bmatrix}, \quad (8)$$

where q is defined as $q = e^{-j\pi/2}$. In other words, the extraction of negative and positive sequences is achieved by using a resonant controller. In the positive sequence, the controller acts similar to the non-ideal resonant controller expressed in (9):

$$G_V = \frac{2K_V\omega_b s}{s^2 + 2\omega_b s + (2\pi f)^2}, \quad (9)$$

where K_V is the gain of resonance controller at fundamental frequency, and ω_b represents the cut-off bandwidth of the controller [23,29,31].

For negative sequence and harmonic compensation, the power quality controller (PQC) can be expressed by the following equation:

$$G_{PQC} = \frac{2K_{UCC}\omega_b s}{s^2 + 2\omega_b s + (2\pi f)^2} (X_{ref,\alpha\beta} - X_{\alpha\beta})^- + (K_P + \frac{2K_{I,3}\omega_b s}{s^2 + 2\omega_b s + (2\pi(3 \times f))^2} + \dots + \frac{2K_{I,h}\omega_b s}{s^2 + 2\omega_b s + (2\pi(h \times f))^2}) (X_{ref,\alpha\beta} - X_{\alpha\beta}), \quad (10)$$

where X_{ref} and X , which are selected by “Mode Selector” switch, can be the reference and actual values of I_2 or V_C , respectively. K_P , K_{UCC} and $K_{I,h}$ represent the proportional coefficient of the resonant controller, and the unbalance and harmonics resonant coefficients of the controller, respectively. Since a low value of K_P is chosen [23], all the components, including fundamental positive and FNS&H components, are decoupled.

In order to damp the resonance of the LCL filter, active and passive damping is utilized. The current of the capacitor or inverter side inductor is usually used for active damping [32,33]. In this paper, the inductor current feedback and a proportional controller (K_D) are employed as the third and damping part of the flexible controller.

3. Control System Design

As discussed before, the control of the inverter is a flexible current-and-voltage-control method. The positive sequence of the V_C is related to the first part of FPQC, and the inverter can be modeled as a VCM inverter. Figure 3 shows the positive sequence voltage generation block. Since negative and harmonic sequences are generated by PQC, this part is eliminated in PS analysis. On the other hand, since the DG-interfacing inverter acts as a CCM inverter in Modes 1, 3 and 4, only PQC and the damping controller can have an influence on the negative and harmonic sequences of DG output current; hence, the control system of the FNS&H current is merely considered as depicted in Figure 4. In these figures, the LCL filter inverter-side inductance and its resistance are represented by L_1 and R_{L1} , respectively, while L_2 and R_{L2} are the grid-side inductance and its resistance, respectively. The capacitor of the LCL filter and its series resistance are shown by C and R_C , respectively.

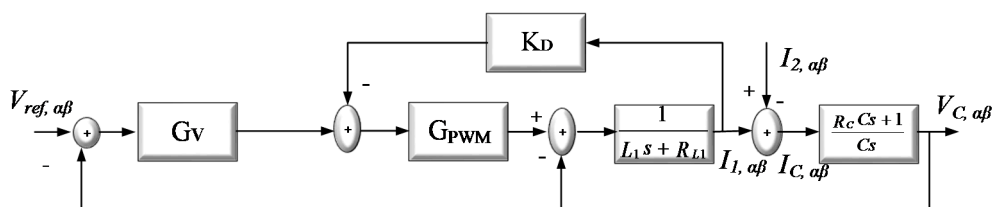


Figure 3. Positive sequence voltage-controller diagram.

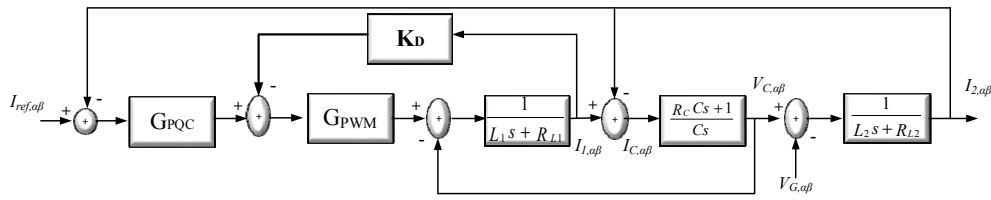


Figure 4. FNS&H current-controller diagram.

In other words, the inverter can be modeled using Norton- and Thevenin-equivalent circuits for FNS&H and positive sequences of fundamental frequency, respectively. As discussed before, in Mode 2 of compensation, the inverter acts as a VCM inverter in negative and harmonic sequences, and its relative model is similar to the PS part depicted in Figure 3.

The closed-loop transfer function of positive sequence capacitor voltage ($G_{PSCV}(s)$) is written by the following equation, according to Figure 3:

$$G_{PSCV} = \frac{V_{C,\alpha\beta}}{V_{ref,\alpha\beta}} = \frac{G_V Z_c / Z_{LI}}{1 + G_V Z_c / Z_{LI} + Z_c / Z_{LI} + K_D / Z_{LI}}, \quad (11)$$

where $Z_{L1} = L_1 s + R_{L1}$ is the impedance of the inverter-side inductance of the LCL filter and its series resistance. $Z_{L2} = L_1 s + R_{L1}$ represents the grid-side inductance and its series resistance, while $Z_C(s) = 1/Cs + R_C$ is the capacitor impedance and its series resistance. G_V is the transfer function of the resonance controller, which is defined according to (9), and K_D is the damping gain, which is shown in Figure 2a.

According to Figure 4, the closed-loop current-transfer function of DG output FNS&H current ($G_{ONHC}(s)$) is written as:

$$G_{ONHC}(s) = \frac{I_{2,\alpha\beta}}{I_{ref,\alpha\beta}} = \frac{G_{PQC} Z_c / (Z_{LI} Z_{L2})}{1 + G_{PQC} Z_c / (Z_{LI} Z_{L2}) + Z_c / Z_{LI} + K_D / Z_{LI} + Z_c / Z_{L2} + K_D Z_c / (Z_{LI} Z_{L2})}, \quad (12)$$

where G_{PQC} represents the power quality compensation controller, which is expressed by Equation (10). Using Equations (11) and (12), the parameters of the controllers can be determined. The cut-off bandwidth of the non-ideal resonant controllers (ω_b) is recommended to be in the range of 5–15 rad/s [31]. In this paper, $\omega_b = 5$ is chosen.

Firstly, the design of the damping part of the controller will be discussed. The Bode diagram of $G_{PSCV}(s)$ is depicted in Figure 5 using the LCL filter parameters in Table 1, $K_V = 700$ and $\omega_b = 5$ rad/s. The Bode diagram is depicted for three amounts of $K_D = 0, 20$ and 100 . As shown in this figure, there is a peak in resonance frequency of the LCL filter when $K_D = 0$. The resonance is damped when K_D is set at 20 and 100. The large amount of K_D may cause additional resonance in the system [34]; thus, $K_D = 20$ is the selected option.

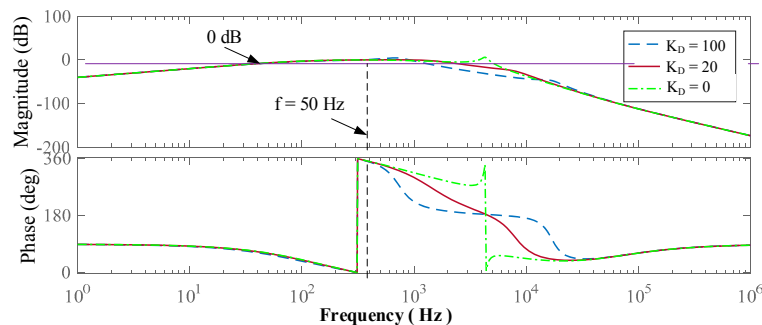


Figure 5. Bode diagram of G_{PSCV} .

In the FNS&H compensation controller, the proportional controller is used for better tracking of the FNS&H voltage or current, and a low value of it is adequate for the controller [31], hence, $K_P = 4$ is chosen. The Bode diagram of the GONHC is shown in Figure 6, considering $K_{UCC} = 700$, $K_{I,3} = 500$, $K_{I,5} = 300$ and $K_{I,7} = 300$. As depicted in this figure, the Bode diagram of the closed-loop transfer function is zero in the fundamental frequency for negative sequence and third-, fifth- and seventh-order harmonics; hence, the current can track its reference value.

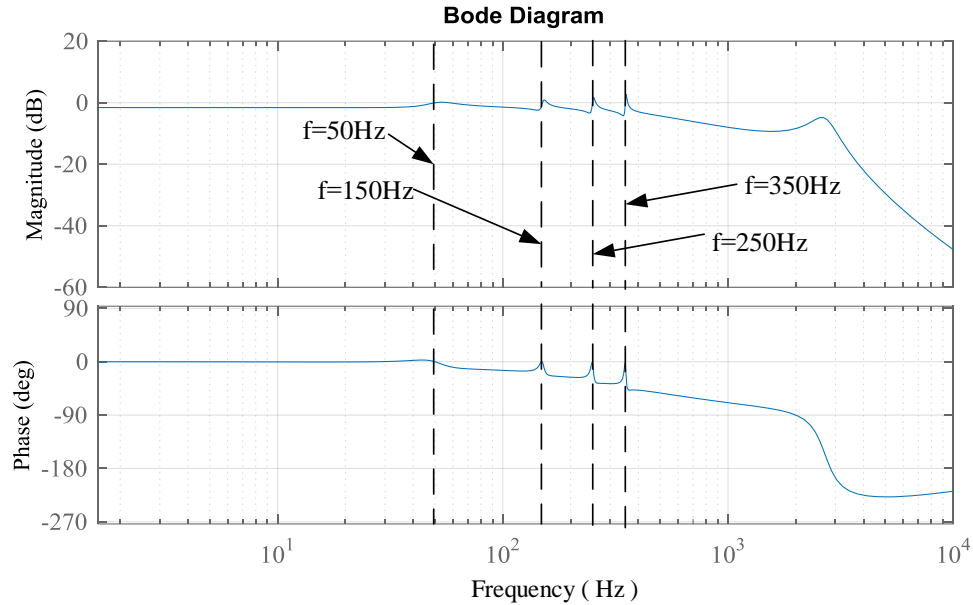


Figure 6. Bode diagram of $G_{ONHC}(s)$.

4. Simulation Results

In this section, the effectiveness of the proposed control method for both grid-connected and islanded MG operation modes is evaluated.

To quantify the voltage and current compensation, the voltage/current unbalance factors (VUF and CUF, respectively) and individual harmonic factors with the following definitions are considered:

$$VUF = \frac{V_{rms}^-}{V_{rms}^+} \times 100, \quad (13)$$

$$CUF = \frac{I_{rms}^-}{I_{rms}^+} \times 100, \quad (14)$$

$$H_h^\pm = \frac{X_{h,rms}^\pm}{X_{1,rms}^+} \times 100, \quad (15)$$

where V_{rms}^-/V_{rms}^+ and I_{rms}^-/I_{rms}^+ are Root Mean Square (RMS) values of negative and positive sequences of voltage/current, respectively. $X_{h,rms}^\pm$ and $X_{1,rms}^+$ represent the harmonic positive and negative sequences and fundamental components of voltage/current variables, respectively.

4.1. Grid-Connected Operation

Figure 7 shows a grid-connected MG, which consists of both local and PCC-connected balanced and unbalanced linear and nonlinear loads. The linear unbalanced (single-phase) load is presented in this figure by R_{unb} . The MG is connected to the grid through the line impedance $Z_{L,G} = R + j\omega L$. It is assumed that the grid voltage is unbalanced with $VUF = 4.5\%$. Control system and power stage

parameters are listed in Table 1. The following five steps are used for evaluating the controller in unbalance and harmonic compensation.

- Step 1 ($0 \text{ s} \leq t < 3.5 \text{ s}$): activation of Mode 1 for V_{PCC} compensation.

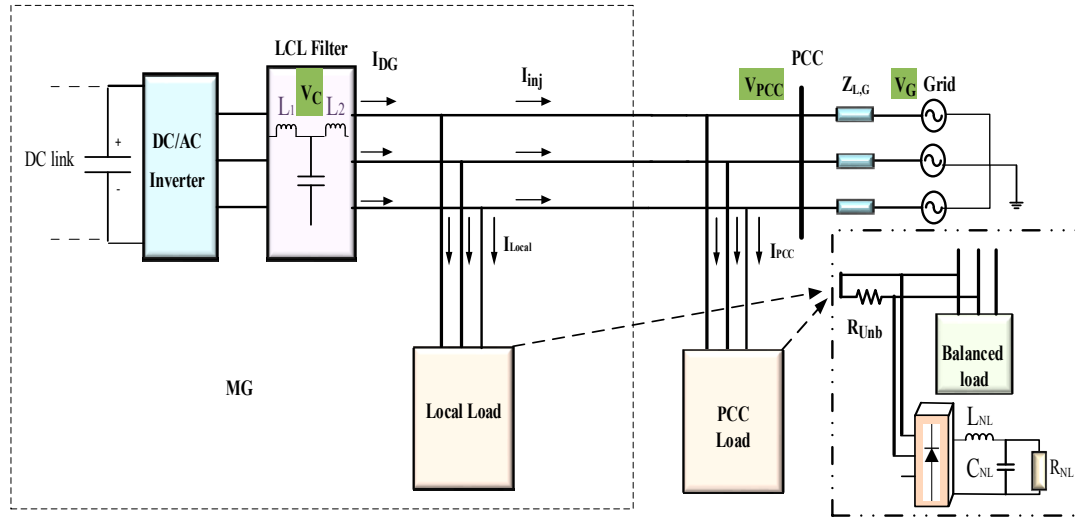


Figure 7. Simulated grid-connected MG with unbalanced linear and nonlinear local and PCC loads.

Table 1. Parameters of grid-connected MG.

DC Link Voltage			LCL Filter ($L_1/C/L_2$)	Voltage/Frequency
650 V			2.5 mH/20 μ F/ 1.5 Mh	230 V/50 Hz
Nonlinear load			Line impedance	Unbalanced linear local and PCC load
C_{NL} (μ F)	R_{NL} (Ω)	L_{NL} (mH)	Z_{LG} (Ω)	R_{unb} (Ω)
235	114	0.084	$0.95j + 1$	20
active and reactive power reference power			Switching frequency	Pulse Width modulation (PWM) delay
Active power reference	Reactive power reference		f_s	T_s
20 kW	5 kVAR		10 kHz	100 μ s
Parameters of controller				
Active power droop coefficients			Reactive power droop coefficients	
m_p	m_l		n_p	n_l
$1 \times 10^{-5} \text{ rad}\cdot\text{s}^{-1}$	$1 \times 10^{-4} \omega$		0.05	0.1
Voltage and current controllers parameters				
K_V	K_{UUC}	$K_{I,3}/K_{I,5}/K_{I,7}$	K_P	K_D
4	700	500/300/300	4	20
Virtual admittance for harmonic and unbalance compensation		Virtual impedance for fundamental component		
G^-	G_h	R_v	L_v	
$25 \Omega^{-1}$	$1 \Omega^{-1}$	0.1Ω	2 mH	

- Step 2 ($3.5 \text{ s} \leq t < 6.5 \text{ s}$): activation of Mode 2 for V_C compensation.
- Step 3 ($6.5 \text{ s} \leq t < 9.5 \text{ s}$): activation of Mode 3 for I_{inj} compensation.

- Step 4 ($9.5 \text{ s} \leq t < 12.5 \text{ s}$): activation of Mode 4 for I_{DG} compensation.
- Step 5 ($12.5 \text{ s} \leq t < 16 \text{ s}$): disabling the harmonic and unbalance compensation (no compensation).

The VUFs and CUFs of the related voltages and currents are depicted in Figures 8 and 9, respectively. Since an unbalanced nonlinear load is used in this study, both sequences of triple and other odd harmonics will be present [35]. Thus, the dominant harmonic components, selected for compensation in this paper, are third, fifth and seventh orders [20]. The amount of the negative and positive sequences of third and fifth harmonics of voltage and current are listed in Table 2 in different steps.

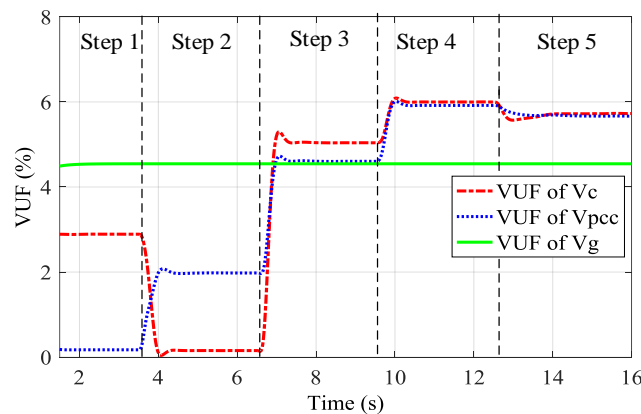


Figure 8. VUF curves of voltages in grid-connected MG.

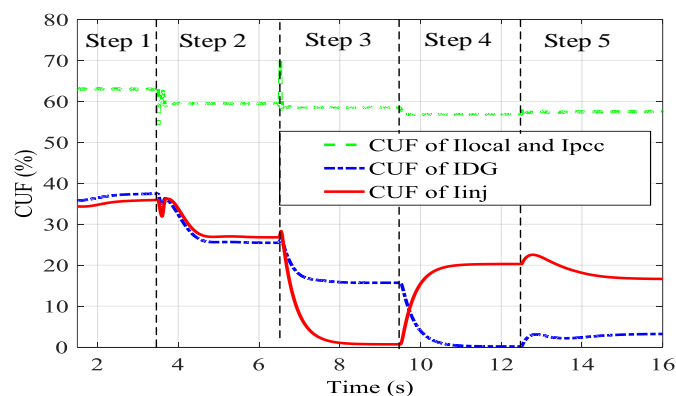


Figure 9. CUF curves of currents in grid-connected MG.

Table 2. Harmonic contents of V_{PCC} , V_C , I_{DG} and I_{inj} in different steps for grid-connected MG.

Steps	Harmonics				V_{PCC}				V_C				I_{DG}				I_{inj}			
	H_3^-	H_3^+	H_5^-	H_5^+	H_3^-	H_3^+	H_5^-	H_5^+	H_3^-	H_3^+	H_5^-	H_5^+	H_3^-	H_3^+	H_5^-	H_5^+	H_3^-	H_3^+	H_5^-	H_5^+
Step 1	1.1	1	0.8	0.8	2.1	2.1	2.2	2.15	8	7.5	5.5	5.25	5	4.9	3.75	3.5				
Step 2	1.5	1.3	1.5	1.2	0.05	0.05	0.05	0.03	6	5.5	3.85	3.25	2.2	2	1.5	1				
Step 3	2.2	1.75	2	1.55	1.5	1.05	1.15	0.9	4.2	3.8	2.3	1.9	0.3	0.2	0.2	0.2				
Step 4	3.75	2.8	2.8	2	3.8	3	2.85	2	0.15	0.08	0.1	0.1	5.3	4.15	2.5	1.75				
Step 5	3.3	2.6	2.5	2	3.15	2.7	2.3	2	1.75	1.4	1.3	1	4	2.9	1.85	1.6				

Note that due to the limitation of paper and table length, the seventh harmonic components are not presented in this Table. Furthermore, the capability of the controller in injecting the reference active and reactive powers to the grid is also investigated. Figure 10 shows that the grid-connected inverter can inject the reference active and reactive powers listed in Table 1 to the grid in different compensation modes. The details of the power quality compensation in the aforementioned steps are explained below:

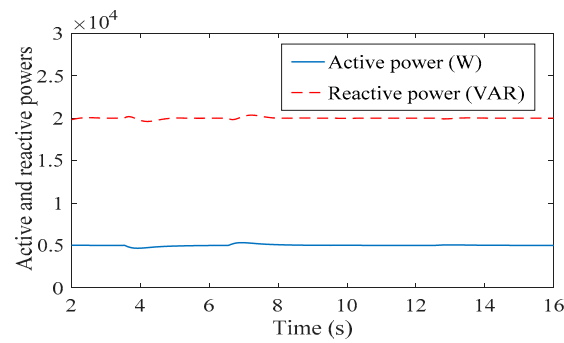


Figure 10. Active and reactive powers of DG.

Step 1: Mode 1 is activated in this step. Since the PCC voltage compensation is the aim of this step and mode, the PCC voltage waveform is depicted in Figure 11. As depicted in Figures 8 and 11, the unbalance voltage of the PCC is compensated and the VUF of the V_{PCC} is about 0.2%, which is below the standard limit of 2%. The compensation is achieved via the negative sequence virtual conductance which creates a low-impedance path for negative sequence current to pass through the DG-interfacing inverter. Therefore, I_{DG} becomes unbalanced, as demonstrated by its CUF in Figure 9. Furthermore, as depicted in Figure 11 and Table 2, the harmonic contents of the PCC voltage are compensated. In order to show the effectiveness of the FPQC in this mode, the PCC voltage is also magnified in Step 5, when the PQC is inactive.

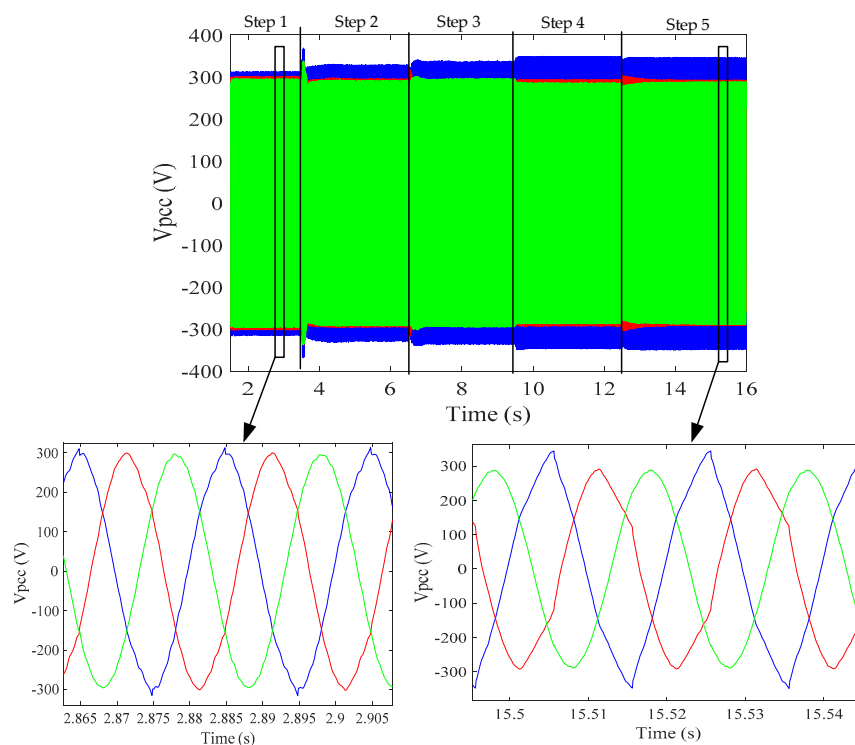


Figure 11. V_{PCC} in Step 1 and Step 5 (without compensation) of unbalance compensation.

Step 2: in this interval, Mode 2 is applied to the inverter by setting the reference value of V_C to 0 in the input of the unbalance compensation controller. The V_C is used as feedback of the FPQC, and negative and harmonic sequence voltages with 180 phase degree difference with negative and harmonics sequences of V_C are injected for compensation. Figure 12 shows the V_C voltage in different

steps, while this figure is magnified in Step 2 (V_C compensation) and Step 5 (without compensation). Furthermore, as depicted in Figure 12 and Table 2, the harmonic compensation of V_C is also achieved.

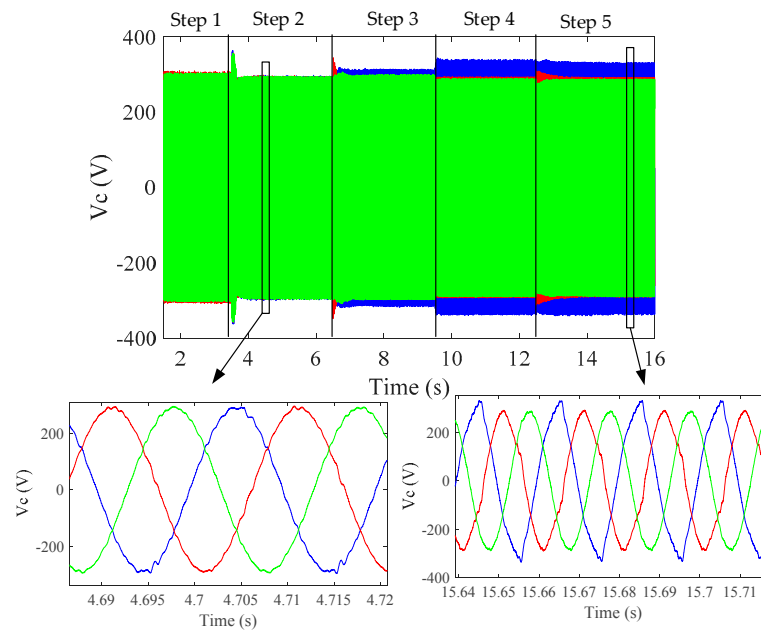


Figure 12. V_C waveform in Step 2 and Step 5 (without compensation) of unbalance compensation.

Step 3: in this interval, Mode 3 of power-quality improvement is selected by the “Mode Selector” switch. In this mode, the $(I_{Local}-I_2)$ is applied to the PQC, as depicted in Figure 2, and the DG generates the negative and harmonic sequences of the local load; hence, I_{inj} will be compensated, as illustrated by CUF (Figure 9), Table 3 and the I_{inj} waveform that is depicted in Figure 13. Figure 13 shows the magnified image of I_{inj} in Step 3 (I_{inj} compensation mode) and Step 5, when the PQC is deactivated for evaluating the performance of power-quality compensation in this mode and step.

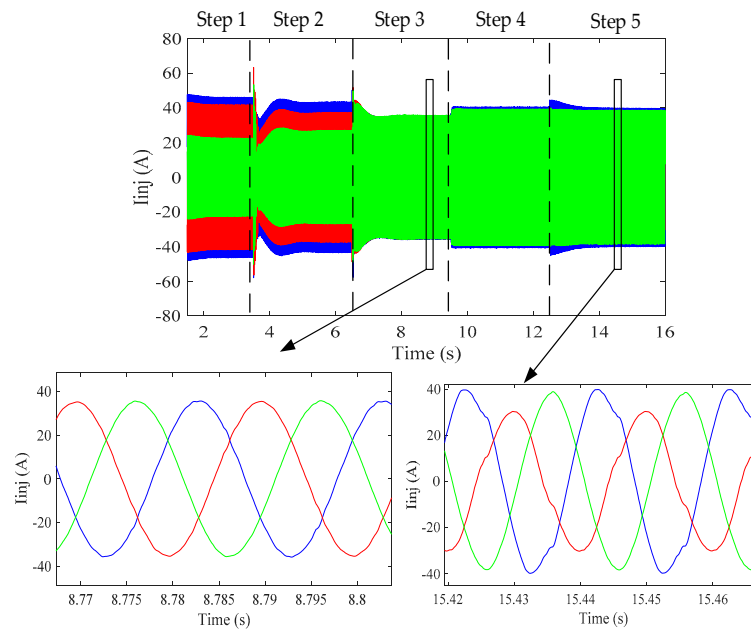


Figure 13. I_{inj} in Step 3 and Step 5 (without compensation) of unbalance compensation.

Step 4: in this mode, which is also called unbalance and harmonic rejection mode, the reference value of the I_2 is chosen to be zero for the PQC controller. As depicted in Figure 9, the CUF of the I_{DG} is approximately zero in this step. This fact is also demonstrated in Figure 14, which shows the waveform of I_{DG} in different steps. In this figure, the waveform is also magnified in Steps 4 (I_{DG} compensation mode) and 5 (without compensation); however, I_{inj} is unbalanced, with a high CUF equal to 30%. Since the unbalanced current passes through the impedances of the grid and the LCL filter, the VUFs of the V_{PCC} and V_C are high in this interval, as depicted in Figure 8. For harmonic compensation, as listed in Table 2, H_3^- , H_3^+ , H_5^- and H_5^+ of V_{PCC} and V_C are increased, and the DG current is also compensated.

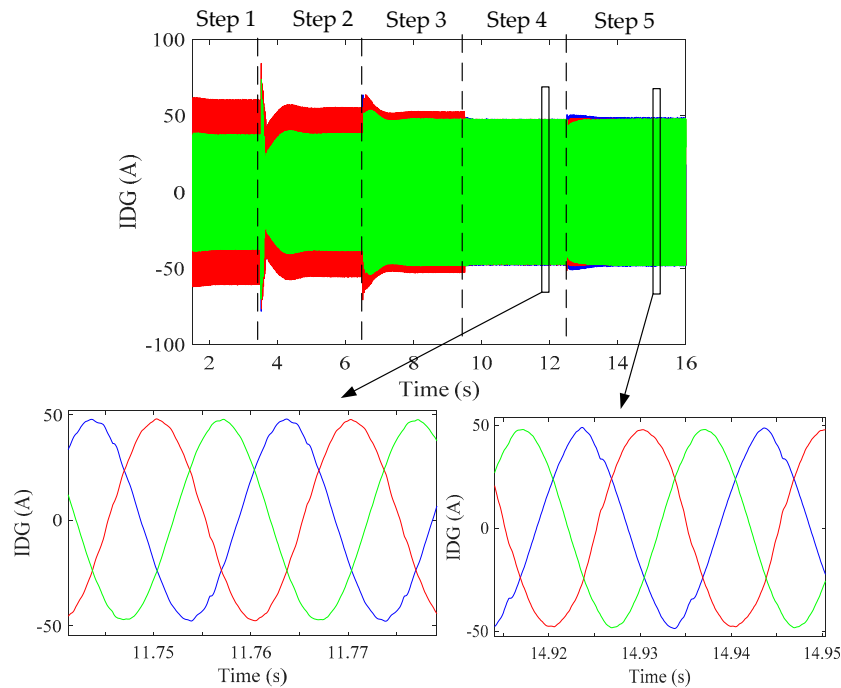


Figure 14. I_{DG} in Step 4 and Step 5 (without compensation) of unbalance compensation.

Step 5: in this interval, the unbalance compensation is deactivated. As shown in Figure 8, the VUF of the V_{PCC} and V_C is approximately 5.5%, which is higher than the standard limit of 2%. In order to show the effectiveness of the FPQC, the waveforms of voltage/current in this step are also depicted in Figures 11–14. This step is used as a comparative study against methods without harmonic and unbalance compensation. For instance, as depicted in Figure 8, the VUF of V_{PCC} and V_C are about 5.8% in this step, which is higher than the standard limit of 2%. The results show the advantage of this method in comparison to the methods proposed in [23,24], in which the negative sequence compensation is not the object of these papers.

4.2. Islanded Operation of the MG

The simulated islanded MG includes two DG units, which supply balanced and unbalanced linear and nonlinear loads, as depicted in Figure 15. The system's parameters are listed in Table 3.

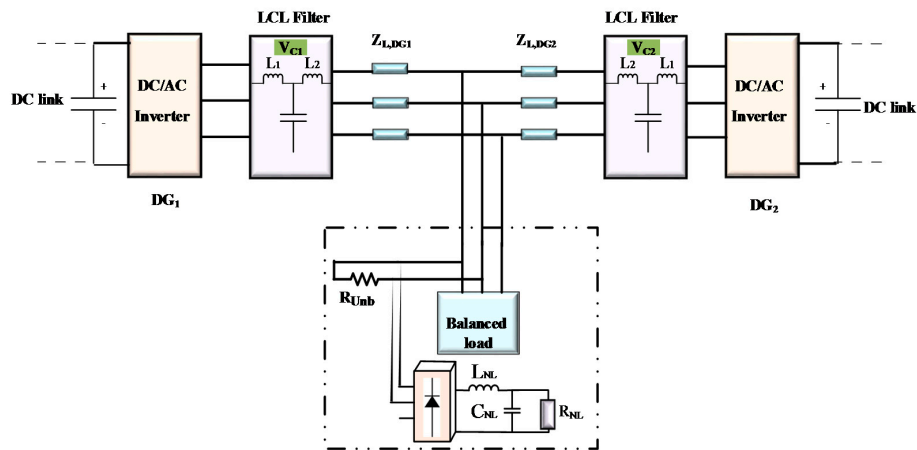


Figure 15. Islanded MG with two DG units.

Table 3. Parameters of the simulated islanded MG.

DC Link Voltage			LCL Filter ($L_1/C/L_2$) for Both DGs	Voltage/Frequency	
650 V			2.5 mH/20 μ F/1.5 mH	230 V/50 Hz	
Nonlinear load			Line impedance for both DGs	Unbalanced linear local and PCC load	
C_{NL} (μ F)	R_{NL} (Ω)	L_{NL} (mH)	Z_{LG} (Ω)	Z_L (Ω)	
235	114	0.084	$0.95j + 1$	40/20	
Parameter of controller					
Active power droops coefficients			Reactive power droops coefficients		
m_p		m_l	n_p	n_l	
$1 \times 10^{-5} \text{ rad}\cdot\text{s}^{-1}$		1×10^{-4}	0.05	0	
Voltage and current controllers parameters					
K_V		K_{UUC}	$K_{I,3}/K_{I,5}/K_{I,7}$	K_P	K_D
4		700	500/300/300	4	20
Virtual admittance for harmonics and unbalance compensation			Virtual impedance for fundamental component		
G^-		G_h	R_v	L_v	
$1.8 \Omega^{-1}$		$0.8 \Omega^{-1}$	0.1Ω	2 mH	

In the case of the islanded MG, two unbalance compensation modes, namely, Mode X and Y, are applied.

Mode X: unbalance and harmonic compensation of PCC.

Mode Y: unbalance and harmonic compensation of V_C .

In order to validate the performance of the controller in islanded operation of MG, the following four steps are used.

✓ Step I:

For evaluating the effectiveness of the controller in improving the power quality of V_{PCC} , the reference of PQC that is depicted in Figure 2 is set as (5), and Mode X is selected for DGs.

✓ Step II:

In this step, the mode of PQC is changed from Mode X to Mode Y. In Mode Y, V_C is used as feedback of the PQC, and its reference value is set to zero. The mode is similar to Mode 2 of the grid-connected inverter. The unbalanced linear load is considered as $R_{Unb} = 40 \Omega$.

✓ Step III:

In this step, Mode Y is selected, and in order to evaluate the performance of the controller in changing the load, at $t = 4$ s the unbalance load is changed from $R_{Unb} = 40 \Omega$ to $R_{Unb} = 20 \Omega$.

✓ Step IV:

In this step, the PQC is deactivated in order to demonstrate the effectiveness of the control method in unbalance and harmonic comparison (compared to the conventional methods without harmonics and unbalance compensation).

Figure 16 shows the VUFs of the capacitor voltages of DG₁ and DG₂ (V_{C1} and V_{C2}), and V_{PCC} , while the harmonic contents of these variables are listed in Table 4. Figure 17 shows V_{PCC} , and this figure is magnified in Step I (PCC compensation mode) and Step IV (without compensation) to evaluate the performance of the PQC. As depicted in Figures 16 and 17 and Table 4, in Step I, the compensation of PCC voltage is achieved; for instance, the VUF of the voltage is below the standard limit of 2% when Mode X is applied.

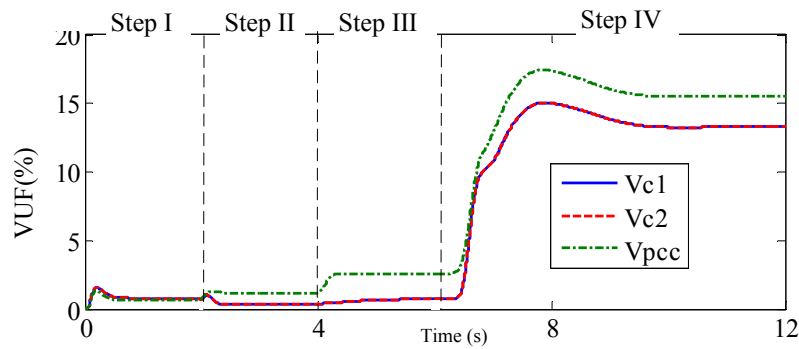


Figure 16. VUFs of related voltages in the islanded microgrid.

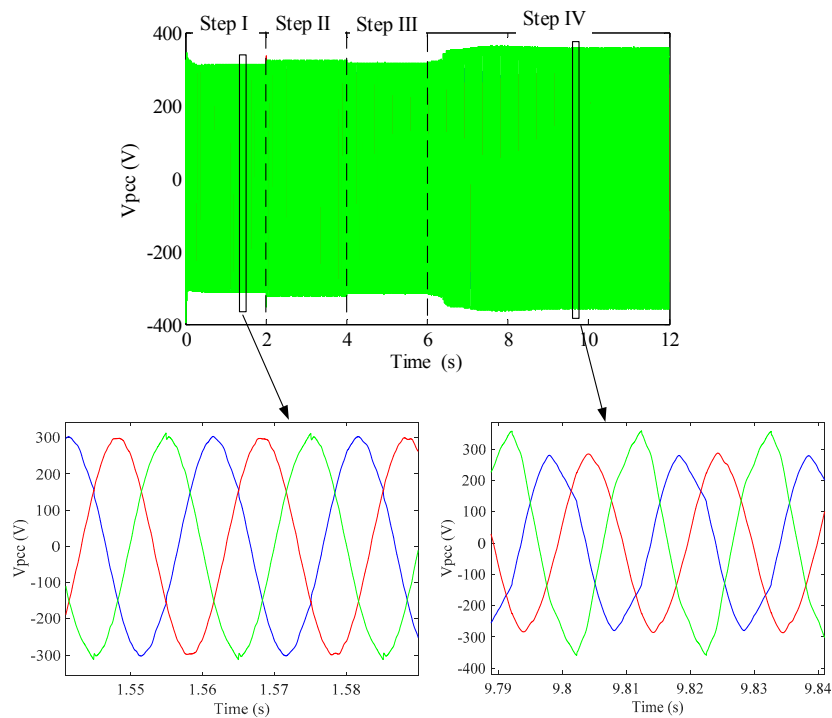
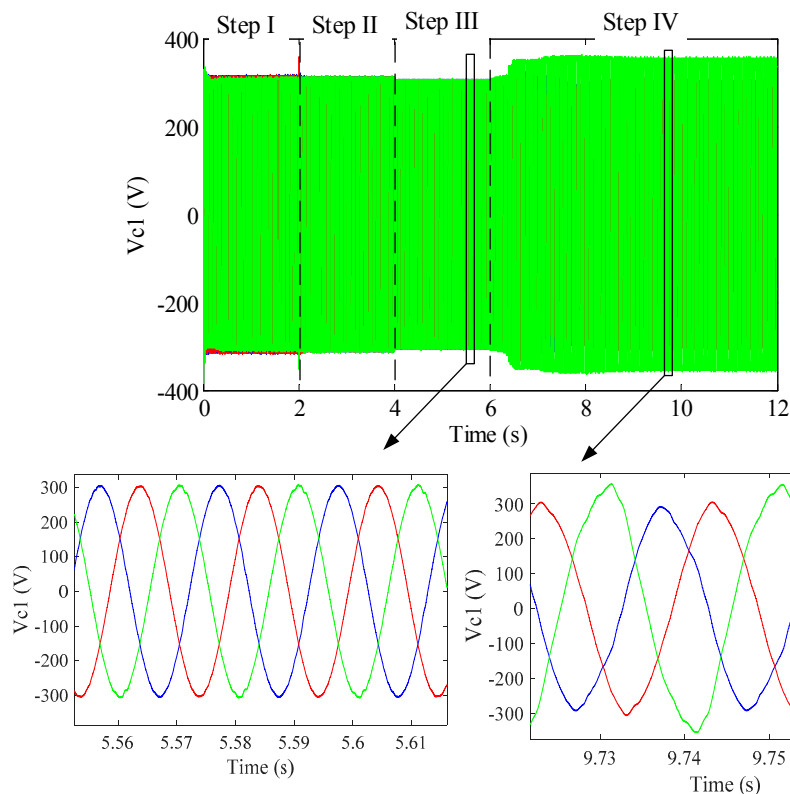


Figure 17. V_{PCC} in Step I (V_{PCC} compensation) and Step IV (without compensation) for islanded MG.

Table 4. Harmonic contents of V_{PCC} , V_{C1} and V_{C2} in different steps for islanded MG.

Steps	Harmonics	V_{PCC}				V_{C1}				V_{C2}			
		H_3^-	H_3^+	H_5^-	H_5^+	H_3^-	H_3^+	H_5^-	H_5^+	H_3^-	H_3^+	H_5^-	H_5^+
Step I		0.65	0.65	0.45	0.45	1.1	0.95	1.05	1.03	1	0.95	1.05	1.04
Step II		1.05	1	1	0.95	0.02	0.042	0.013	0.02	0.015	0.04	0.015	0.015
Step III		1.1	1.05	1.05	0.9	0.025	0.05	0.018	0.03	0.02	0.05	0.02	0.025
Step IV		4	1	1.2	1.6	3.3	3	0.6	1.05	3.3	3	0.6	1.5

Furthermore, Figure 16 shows that the VUFs of V_{C1} and V_{C2} are improved in the Steps II and III, while the harmonic compensation becomes evident by selecting Mode Y, as presented in Table 4. In addition, according to Figure 16, the controller can compensate the negative sequence of the V_{C1} and V_{C2} when the load is doubled at $t = 4$ s. The harmonic contents of the V_{C1} and V_{C2} are compensated, according to Table 4. This is also shown in Figure 18, which depicts V_{C1} waveform in Steps III (V_C compensation mode) and IV (without harmonics and unbalance compensation). As depicted in Figures 16–18, if the unbalance compensation block is deactivated, the unbalance voltage problem can lead to some voltage quality problem for the microgrid. This shows the effectiveness of this applied method, in comparison to methods proposed in [23,24], in which the DGs can contribute only in harmonics compensation in a single-phase microgrid.

**Figure 18.** V_{C1} in Step III (V_C compensation) and Step IV (without compensation) for islanded MG.

5. Conclusions

In this paper, a flexible unbalance and harmonic compensation scheme is proposed for the improvement of current and voltage quality in MGs. The performance of the method is investigated in both grid-connected and islanded operations of MGs. The simulation results show that by applying the proposed control scheme to a grid-connected MG, not only the active and reactive powers injection is achieved, but also control modes for harmonics and unbalance compensation of PCC voltage, DG voltages, DG output current or injected current of MGs can be obtained. According to the results,

by selecting the PCC compensation mode, the VUF, H_3^+ and H_3^- of the PCC voltage are mitigated to about 0.2%, 1% and 1.1%, respectively, which are under the limitation of EN 50160 and IEEE 519 standards while these variables are approximately 5.8%, 3.3% and 2.6%, respectively, when the power-quality compensation is not activated (the case of conventional inverter control).

In order to validate the performance of the control system in islanded operation, the control method is applied to an MG which consists of two inverter-based DGs and unbalanced linear and nonlinear loads. The simulation results show that the PCC or DG voltages harmonic and unbalance compensation can be achieved, depending on the desired control goal. For better evaluation of the performance of the control method in harmonic and unbalance compensation, this control method is deactivated in one step. The results show that the VUF, H_3^- and H_3^+ of DG output voltages are approximately 0.3%, 0.025% and 0.05%, respectively, when the DG output voltages compensation is activated; furthermore, the results show that when the controller is deactivated, VUF, H_3^- and H_3^+ of DG output voltages are 13%, 3.3% and 3%, respectively.

By using the proposed controller, the DGs can contribute in unbalance and harmonic compensation of voltage or current, depending on the selected compensation mode. The proposed control can reduce the need for dedicated additional power-quality conditioners. For future work, the coordinated control of multiple DGs equipped with this control method can be examined.

Author Contributions: The design of the controller and simulation of the case study has been done by Seyyed Yousef Mousazadeh Mousavi under supervision of Alireza Jalilian and co-supervision of Mehdi Savaghebi and Josep M. Guerrero. The manuscript has been written by Seyyed Yousef Mousazadeh Mousavi and has been revised by other authors.

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